

a peripheral partitioning light shielding film formed in an outer peripheral of the display region;

a plurality of conductive first light shielding films for shielding respective channel regions of the thin film transistors, the plurality of conductive first light shielding films being formed below at least the respective channel regions of the thin film transistors so as to extend along at least one of the scanning line and the data line; and

a constant potential wiring electrically connected to the conductive first light shielding films, the films being connected with each other and below the peripheral partitioning light shielding film.--

claim 34
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claim 35
--35. The liquid crystal device according to claim 34, the constant potential wiring being disposed below the peripheral partitioning light shielding film, and an end of the wiring portion being connected to the constant potential wiring.--

--36. The liquid crystal device according to claim 35, the constant potential wiring and the wiring portion being connected to each other via a contact hole.--

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--37. The liquid crystal device according to claim 36, wherein the contact hole is positioned below a region of the peripheral partitioning light shielding film which is extended from a plurality of contact holes, and which is connected to the data lines and the thin film transistors.--

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--38. The liquid crystal device according to claim 35, the constant potential wiring and the wiring portion being connected via a junction electrode.--

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--39. The liquid crystal device according to claim 38, wherein a contact hole connecting the constant potential wiring and the junction electrode and a contact hole connecting the wiring portion and the junction electrode are positioned offset with respect to each other.--